

## Electronics II

Lecture 02
Transistor Modeling as an Amplifier

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The theme of this presentation is an inspiration from the one used in S2 Department of Chalmers University of Technology, Gothenburg, Sweden.

## Session Overview

| Topic | Transistor Modeling as an Amplifier |
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| Concepts | Transistor Amplifying Action, Two Port Network and <br> Parameters, Input Impedance, Output Impedance, Voltage <br> Gain, Current Gain. |
| Recommended <br> Reading | Sections 7.1 through 7.4 of [1] |
| Keywords | Amplifier, Input Impedance, Output Impedance, Current Gain, <br> Voltage Gain, Phase Relationship. |

## Amplification in AC Domain

- Transistor can be used as an amplifier. Basic amplifying action is that the output AC power is greater than the input AC power.
- This increase in output power is achieved at the expense of DC
 power.
- Conversion Efficiency , $\eta$
$\eta=\mathrm{P}_{\mathrm{o} \text { (ac) }} / \mathrm{P}_{\mathrm{i} \text { (dc) }}$



## BJT Transistor Modeling

- The major step of small signal analysis is to determine the ac equivalent circuit of the device.
- This ac equivalent model replaces the schematic symbol within the circuit and the analysis is performed.
- There are two ac equivalent models - hybrid model and re model.
- For AC analysis, all the DC power supplies can be replaced by short circuit.
- All coupling and by- pass capacitors are assumed to have very small reactance at the frequency of interest, and they are replaced by low resistance paths/ short circuit.
- This will cause a by-pass/ short circuit for the emitter resistance Re.
- Which one is suitable?


## BJT Transistor Modeling



Robert L Boylestad, Electronic Devices and Circuit Theory, $8^{\text {th }}$ Edition, Pearson Education Inc, ISBN: 81-7808-590-9


## BJT Transistor Modeling



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1. Set all dc sources to zero and replace these by short circuit equivalent.
2. Replace all capacitors by short circuit equivalent.
3. Remove all elements bypassed by the short circuit equivalents replaced as a result of steps 01 and 02 .
4. Redraw the network in a more convenient way.

## COMSATS Electronics II Important Parameters of Two Port Systems



## Input Impedance, $\mathrm{Z}_{\mathrm{i}}$

- Small signal AC input impedance $\mathrm{Z}_{\mathrm{i}}$ can be calculated as
$\mathrm{Z}_{\mathrm{i}}=\mathrm{V}_{\mathrm{i}} / \mathrm{I}_{\mathrm{i}}$.
- For small signal analysis, once $Z_{i}$ is determined, same value can be used for the varying levels of input signals.
- Input impedance of a transistor can approximately be determined by DC biasing conditions.
- For frequencies in low to midrange (typically $\leq 100 \mathrm{KHz}$ )
- $Z_{i}$ of BJT amplifier is purely resistive and vary from few Ohms to Mega- Ohms.
- An ohm-meter cannot be used to measure small signal ac input impedance.(Why?)



## Input Impedance, $\mathrm{Z}_{\mathrm{i}}$

- Input impedance $Z_{i}$ can be - Significance of input impedance calculated as
$\mathrm{li}=\left(\mathrm{Vs}-\mathrm{V}_{\mathrm{i}}\right) /$ Rsense.
$\mathrm{Z}_{\mathrm{i}}=\mathrm{V}_{\mathrm{i}} / \mathrm{I}_{\mathrm{i}}$.



## Input Impedance, $\mathrm{Z}_{\mathrm{i}}$

- Example 7.1 (Boylestad):
- Determine the Input impedance of the given two port system.


Robert L. Boylestad, Electronic Devices and Circuit Theory, $8^{\text {th }}$ Edition, Pearson Education Inc, ISBN: 81-7808-590-9.

## Output Impedance, $\mathrm{Z}_{\mathrm{o}}$

- The small signal AC impedance measured at the output terminals of a two port system is referred to as Output Impedance, $\mathrm{Z}_{\mathrm{o}}$.
- $Z_{0}$ is determined by looking back into the output terminals and setting the applied signal to zero.
- $\quad \mathrm{lo}=(\mathrm{V}-\mathrm{Vo}) /$ Rsense.
- $Z_{o}=V_{o} / l_{0}$.
- For low to mid range frequencies (typically $\leq 100 \mathrm{KHz}$ ) the output impedance of a BJT amplifier is resistive in nature.
- $Z_{o}$ cannot be determined by Ohm-meter.


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## Output Impedance, $\mathrm{Z}_{\mathrm{o}}$

- For a significant current gain, $Z_{o}$ should be much greater that the load resistance, $\mathrm{R}_{\mathrm{L}}$.
- Why?
 Robert L. Boylestad, Electronic Devices and Circuit Theory, $8^{\text {th }}$ Edition, Pearson Education Inc, ISBN: 81-7808-590-9.
- Example 7.2 (Boylestad): Determine $\mathrm{Z}_{\mathrm{o}}$.


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## References

[1] Robert L. Boylestad, Electronic Devices and Circuit Theory, $8^{\text {th }}$ Edition, Pearson Education Inc, ISBN: 81-7808-590-9.

