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## **Electronics II**

Lecture 02 Transistor Modeling as an Amplifier

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## **Session Overview**

Торіс	Transistor Modeling as an Amplifier	
Concepts	epts Transistor Amplifying Action, Two Port Network and Parameters, Input Impedance, Output Impedance, Voltage Gain, Current Gain.	
Recommended Reading	Sections 7.1 through 7.4 of [1]	
Keywords	Amplifier, Input Impedance, Output Impedance, Current Gain, Voltage Gain, Phase Relationship.	

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- The major step of small signal analysis is to determine the ac equivalent circuit of the device.
- This ac equivalent model replaces the schematic symbol within the circuit and the analysis is performed.
- There are two ac equivalent models – hybrid model and re model.
- Which one is suitable?

- For AC analysis, all the DC power supplies can be replaced by short circuit.
- All coupling and by- pass capacitors are assumed to have very small reactance at the frequency of interest, and they are replaced by low resistance paths/ short circuit.
- This will cause a by-pass/ short circuit for the emitter resistance R<sub>E</sub>.

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BJT Transistor Modeling					
	$\begin{array}{c} & & & & \\ & & & \\ R_{1} & & & \\ R_{2} & & & \\ & & & & \\ & & & \\ & & & & & \\ & & & & \\ & & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & & \\$				
1.	Set all dc sources to zero and replace these by short circuit equivalent.				
2.	Replace all capacitors by short circuit equivalent.				

- 3. Remove all elements bypassed by the short circuit equivalents replaced as a result of steps 01 and 02.
- 4. Redraw the network in a more convenient way.

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Input Impedance, Z <sub>i</sub>						
• Example 7.1 (Boylestad):	<ul> <li>Determine the Input impedance of the given two port system.</li> </ul>					
	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$					
	Robert L. Boylestad, <i>Electronic Devices and Circuit Theory</i> , 8 <sup>th</sup> Edition, Pearson Education Inc, ISBN: 81-7808-590-9.					
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Output Impedance, Z <sub>o</sub>					
•	For a significant current gain, $\cdot$ $Z_o$ should be much greater that the load resistance, $R_L$ .	Example 7.2 (Boylestad): Determine Z <sub>o</sub> .			
•	Why?				
	$Z_{\phi} = R_{\phi} $ $I_{R_{\phi}}$ $R_{L} $ For $R_{\phi} \gg R_{L}$ $I_{L} \gg I_{R_{\phi}}$	Two-port System $V_{z} = 0 V$ $P_{z} = 0 $			
	Robert L. Boylestad, <i>Electronic Devices and Circuit Theory,</i> 8 <sup>th</sup> Edition, Pearson Education Inc., ISBN: 81-7808-590-9.	Robert L. Boylestad, <i>Electronic Devices and Circuit Theory,</i> 8 <sup>th</sup> Edition, Pearson Education Inc, ISBN: 81-7808-590-9.			
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